

<b>Notice of References Cited</b>	Application/Control No. 09/895,524	Applicant(s)/Patent Under Reexamination MENA ET AL.	
	Examiner George Fourson	Art Unit 2823	Page 1 of 1

**U.S. PATENT DOCUMENTS**

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
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	B	US-6,197,691	03-2001	Lee, James Yong Meng	438/691
	C	US-			
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	J	US-			
	K	US-			
	L	US-			
	M	US-			

**FOREIGN PATENT DOCUMENTS**

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**NON-PATENT DOCUMENTS**

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Wolf, S., "Silicon Processing for the VLSI Era - Vol.2, Process Integration", Lattice Press, 1990, pp.194-197 and 232-235.
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\*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)  
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